

Profile-Guided Microarchitectural Floorplanning for Deep Submicron Processor Design

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Regular Fabrics In Deep Submicron Integratedcircuit Design

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Regular Fabrics in Deep Sub-Micron Integrated-Circuit Design Fan Mo,Robert K. Brayton,2013-03-28 Regular Fabrics in Deep Sub Micron Integrated Circuit Design discusses new approaches to better timing closure and manufacturability of DSM Integrated Circuits The key idea presented is the use of regular circuit and interconnect structures such that area delay can be predicted with high accuracy The co design of structures and algorithms allows great opportunities for achieving better final results thus closing the gap between IC and CAD designers The regularities also provide simpler and possibly better manufacturability In this book we present not only algorithms for solving particular sub problems but also systematic ways of organizing different algorithms in a flow to solve the design problem as a whole A timing driven chip design flow is developed based on the new structures and their design algorithms which produces faster chips in a shorter time Proceedings ,2007

Field-Programmable Logic and Applications Peter Y.K. Cheung,Georg A. Constantinides,2003-08-27 This book constitutes the refereed proceedings of the 13th International Conference on Field Programmable Logic and Applications FPL 2003 held in Lisbon Portugal in September 2003 The 90 revised full papers and 56 revised poster papers presented were carefully reviewed and selected from 216 submissions The papers are organized in topical sections on technologies and trends communications applications high level design tools reconfigurable architecture cryptographic applications multi context FPGAs low power issues run time reconfiguration compilation tools asynchronous techniques bio related applications codesign reconfigurable fabrics image processing applications SAT techniques application specific architectures DSP applications dynamic reconfiguration SoC architectures emulation cache design arithmetic bio inspired design SoC design cellular applications fault analysis and network applications American Book Publishing Record ,2004 *FPGA ...* ,2005

Routing Issues in Nanometer-scale Integrated Circuits Tianpei Zhang,2006 Digest of Technical Papers ,2001

Dissertation Abstracts International ,2004 **18th International Conference on VLSI Design** International Conference on VLSI Design,2005 **On-Chip Communication Architectures** Sudeep Pasricha,Nikil Dutt,2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication

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Low-Power Electronics Design Christian Piquet,2018-10-03 The power consumption of integrated circuits is one of the most problematic considerations affecting the design of high performance chips and portable devices The study of power saving design methodologies now must also include subjects such as systems on chips embedded software and the future of microelectronics Low Power Electronics Design covers all major aspects of low power design of ICs in deep submicron technologies and addresses emerging topics related to future design This volume explores in individual chapters written by expert authors the many low power techniques born during the past decade It also discusses the many different domains and disciplines that impact power consumption including processors complex circuits software CAD tools and energy sources and management The authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality They investigate nanotechnologies optical circuits ad hoc networks e textiles as well as human powered sources of energy Low Power Electronics Design delivers a complete picture of today s methods for reducing power and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now *Chemical Abstracts* ,2002 **Proceedings of SLIP '07** ,2007 **ISMVL 2004** International Symposium on Multiple Valued Logic (34, 2004, Toronto),IEEE Computer Society,2004 **Electrical & Electronics Abstracts** ,1994 **Memorandum** ,2001 **Books In Print 2004-2005** Ed Bowker Staff,Staff Bowker, Ed,2004 **CODES+ISSS ...** ,2006 *American Doctoral Dissertations* ,1999

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