



Network Processors

Michael Seilmaier



Network Processors:

Network Processors Ran Giladi, 2008-08-29 Network processors are the basic building blocks of today's high speed high demand quality oriented communication networks Designing and implementing network processors requires a new programming paradigm and an in depth understanding of network processing requirements This book leads the reader through the requirements and the underlying theory of networks network processing and network processors It covers implementation of network processors and integrates EZchip Microcode Development Environment so that you can gain hands on experience in writing high speed networking applications By the end of the book the reader will be able to write and test applications on a simulated network processor Comprehensive theoretical and practical coverage of networks and high speed networking applications Describes contemporary core metro and access networks and their processing algorithms Covers network processor architectures and programming models enabling readers to assess the optimal network processor type and configuration for their application Free download from <http://www.cse.bgu.ac.il/~npbook/> includes microcode development tools that provide hands on experience with programming a network processor

Network Processor Design Mark A. Franklin, Patrick Crowley, Haldun Hadimioglu, Peter Z. Onufryk, 2002-10-16 As the demand for digital communication networks has increased so have the challenges in network component design To meet ever escalating performance flexibility and economy requirements the networking industry has opted to build products around network processors These new chips range from task specific processors such as classification and encryption engines to more general purpose packet or communications processors Programmable yet application specific their designs are tailored to efficiently implement communications applications such as routing protocol analysis voice and data convergence firewalls VPNs and QoS Network processor design is an emerging field with issues and opportunities both numerous and formidable To help meet this challenge the editors of this volume created the first Workshop on Network Processors a forum for scientists and engineers from academia and industry to discuss their latest research in the architecture design programming and use of these devices In addition to including the results of the Workshop in this volume the editors also present specially commissioned material from practicing designers who discuss their companies latest network processors *Network Processor Design Issues and Practices* is an essential reference on network processors for graduate students researchers and practicing designers Includes contributions from major academic and industrial research labs including Aachen University of Technology Cisco Systems Infineon Technologies Intel Corp North Carolina State University Swiss Federal Institute of Technology University of California Berkeley University of Dortmund University of Washington and Washington University Examines the latest network processors from Agere Systems Cisco IBM Intel Motorola Sierra Inc and TranSwitch

Network Processor Design Mark A. Franklin, Patrick Crowley, Haldun Hadimioglu, Peter Z. Onufryk, 2005-03-11 The past few years have seen significant change in the landscape of high end network processing In response to the formidable

challenges facing this emerging field the editors of this series set out to survey the latest research and practices in the design programming and use of network processors Through chapters on hardware software performance and modeling Network Processor Design illustrates the potential for new NP applications helping to lay a theoretical foundation for the architecture evaluation and programming of networking processors Like Volume 2 of the series Volume 3 further shifts the focus from achieving higher levels of packet processing performance to addressing other critical factors such as ease of programming application developments power and performance prediction In addition Volume 3 emphasizes forward looking leading edge research in the areas of architecture tools and techniques and applications such as high speed intrusion detection and prevention system design and the implementation of new interconnect standards Investigates current applications of network processor technology at Intel Infineon Technologies and NetModule Presents current research in network processor design in three distinct areas Architecture at Washington University St Louis Oregon Health and Science University University of Georgia and North Carolina State University Tools and Techniques at University of Texas Austin Academy of Sciences China University of Paderborn Germany and University of Massachusetts Amherst Applications at University of California Berkeley Universidad Complutense de Madrid Spain ETH Zurich Switzerland Georgia Institute of Technology Vrije Universiteit the Netherlands and Universiteit Leiden the Netherlands Network Processors : Architectures, Protocols and Platforms Panos Lekkas,2003-07-28 Network processing units NPUs will be the occasion of sweeping changes in the network hardware industry over the next few years This new breed of microchip impacts chip designers like Intel equipment vendors like Cisco application developers like IBM and Motorola and an army of software engineers who spent the last decade working on protocols and network management solutions A thoroughly practical dissection of the early NPU market this designer s guide explains how network processors work and provides detailed information on all major commercial architectures from features to design considerations Comparative tables are a rich source of cross industry info Coverage includes traffic managers classification chips content addressable memories switch fabrics security accelerators storage coprocessors and NetASICs *Network Systems Design* Douglas Comer,2004 Network System Design Using Network Processors is the right book at the right time Networking expert Douglas Comer divides this book into four major sections a quick review of basics and packet header formats Traditional Protocol Processing Systems Network Processors an independent overview of the technology including motivation economics inherent complexities and various examples of commercial architectures and Intel s network processor Network processor complexity is boiled down and simplified by allowing readers to see example code for a commercial processor detailed explanations on the motivation and economics behind the technology and a glossary for quick reference The book s scope includes the concepts principles and hardware and software architectures that are the underpinnings of the design and implementation of network systems including routers bridges switches intrusion detection systems and firewalls all independent of vendor specifics An excellent fusion of network processing design principles current

architectures and architectural directions it is sure to become the standard text for this field the minute it hits the shelves

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