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Thakkar, S. S.

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D. B. Skillicorn



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Selected Reprints on Dataflow and Reduction Architectures S. S. Thakkar, 1987 **VLSI for Artificial Intelligence and Neural Networks** Jose G. Delgado-Frias, W.R. Moore, 2012-12-06 This book is an edited selection of the papers presented at the International Workshop on VLSI for Artificial Intelligence and Neural Networks which was held at the University of Oxford in September 1990 Our thanks go to all the contributors and especially to the programme committee for all their hard work Thanks are also due to the ACM SIGARCH the IEEE Computer Society and the IEE for publicizing the event and to the University of Oxford and SUNY Binghamton for their active support We are particularly grateful to Anna Morris Maureen Doherty and Laura Duffy for coping with the administrative problems Jose Delgado Frias Will Moore April 1991 vii PROLOGUE Artificial intelligence and neural network algorithms computing have increased in complexity as well as in the number of applications This in turn has posed a tremendous need for a larger computational power than can be provided by conventional scalar processors which are oriented towards numeric and data manipulations Due to the artificial intelligence requirements symbolic manipulation knowledge representation non deterministic computations and dynamic resource allocation and neural network computing approach non programming and learning a different set of constraints and demands are imposed on the computer architectures for these applications **Proceedings of the 1993 International Conference on Parallel Processing** Salim Hariri, P. Bruce Berra, 1993-08-16 This three volume work presents a compendium of current and seminal papers on parallel distributed processing offered at the 22nd International Conference on Parallel Processing held August 16 20 1993 in Chicago Illinois Topics include processor architectures mapping algorithms to parallel systems performance evaluations fault diagnosis recovery and tolerance cube networks portable software synchronization compilers hypercube computing and image processing and graphics Computer professionals in parallel processing distributed systems and software engineering will find this book essential to complete their computer reference library TAPSOFT '89. Proceedings of the International Joint Conference on Theory and Practice of Software Development Barcelona, Spain, March 13-17, 1989 Josep Diaz, Fernando Orejas, 1989-02-27 TAPSOFT 89 is the Third International Joint Conference on Theory and Practice of Software Development held in Barcelona Spain March 13 17 1989 The conference consisted of three parts Advanced Seminar on Foundations of Innovative Software Development Colloquium on Trees in Algebra and Programming CAAP 89 Colloquium on Current Issues in Programming Languages CCIPL The TAPSOFT 89 Conference Proceedings are published in two volumes The first volume includes the papers from CAAP plus the more theoretical ones of the invited papers The second volume comprises the papers from CCIPL and the invited papers more relevant to current issues in programming languages *Advances in Computing and Information - ICCI '91* Frank Dehne, F. Fiala, 1991-05-07 This volume contains papers presented at the Third International Conference on Computing and Information ICCI 91 held at Carleton University in Ottawa Canada May 27 29 1991 The conference was organized by the

School of Computer Science at Carleton University and was sponsored by the Natural Sciences and Engineering Research Council of Canada NSERC and Carleton University ICCI 91 was an international forum for the presentation of original results in research development and applications in computing and information processing The conference was aimed at both practitioners and theoreticians and was organized into five streams Algorithms and complexity Databases and information systems Parallel processing and systems Distributed computing and systems Expert systems artificial intelligence This volume contains three invited papers by E C R Hehner R L Probert and S J Smith and 71 selected papers

Multidimensional Programming Edward A. Ashcroft, 1995 This book describes a powerful language for multidimensional declarative programming called Lucid Lucid has evolved considerably in the past ten years The main catalyst for this metamorphosis was the discovery that Lucid is based on intensional logic one commonly used in studying natural languages Intensionality and more specifically indexicality has enabled Lucid to implicitly express multidimensional objects that change a fundamental capability with several consequences which are explored in this book The author covers a broad range of topics from foundations to applications and from implementations to implications The role of intensional logic in Lucid as well as its consequences for programming in general is discussed The syntax and mathematical semantics of the language are given and its ability to be used as a formal system for transformation and verification is presented The use of Lucid in both multidimensional applications programming and software systems construction such as a parallel programming system and a visual programming system is described A novel model of multidimensional computation education is described along with its serendipitous practical benefits for harnessing parallelism and tolerating faults As the only volume that reflects the advances over the past decade this work will be of great interest to researchers and advanced students involved with declarative language systems and programming

Foundations of Parallel Programming D. B. Skillicorn, 1994-12 The major reason for the lack of use of parallel computing is the mismatch between the complexity and variety of parallel hardware and the software development tools to program it The cost of the developing software needs to be amortised over decades but the platforms on which it executes change every few years requiring complete rewrites The evident cost effectiveness of parallel computation has not been realized because of this mismatch This book presents an integrated approach to parallel software development that addresses software issues and performance issues together It presents a methodology for software construction that produces software that is architecture independent and intellectually abstract The software can execute efficiently on a range of existing and potential hardware configurations The approach is based on the construction of categorical data types a generalisation of abstract data types and of objects Categorical data types abstract both from the representation of a data type and also from the detailed control flow necessary to perform operations on it They thus impose a strong separation between the semantics on which programs can depend and the implementation which is therefore free to hide the parallel machine properties that are used

Parallel Architectures and Compilation Techniques Michel

Cosnard,Guang R. Gao,Gabriel M. Silberman,1994 Fine and medium grain parallelism continues to hold its own as a vital vibrant research topic Within the area new developments in superscalar and VLIW architectures and their associated compilation techniques have provided exciting new avenues to extract performance from a slowing technology curve Comprising 28 full length papers and a number of short poster papers this publication offers a high quality exploration of the current state of the art in the field It will be of particular interest to those involved in control structures and microprogramming processor architectures computer system implementation programming techniques and software engineering Euro-Par'98 Parallel Processing David Pritchard,1998-08-19 Proceedings Parallel Computing

Telecommunication Journal ,1988 **Computer Architecture '97** Ronald Pose,1997-04-01 The Australasian Computer Architecture Conferences were established in recognition of the fundamental reliance of all computer systems on the integrity capability and performance of the underlying computer architecture ACAC 97 held in February 1997 in Sydney Australia was the second in this series and attracted authors from Singapore Hong Kong Korea Sweden New Zealand Spain China the United States of America and Australia This volume contains a selection of 21 research papers the best and most interesting of those presented at the conference highlighting technologies to increase processor performance asynchronous processor designs interconnection networks and routing and parallel and distributed multiprocessor systems

Multiprocessor Computer Architectures Terry J. Fountain,Malcolm J. Shute,1990 For over forty years most of the world has designed its computers after a single fashion This style which has come to be known as the von Neumann model after one of its originators is showing increasing signs of being inadequately powerful for future needs Efforts to discover alternative models for so long a trickle are at least becoming a flood This book reports on the current status of a number of these efforts This book is addressed to two main audiences First it is aimed at the reader who is new to the world of non von Neumann computers either in the area of design or that of application By taking the form of a catalogue of projects bound in a single volume it serves as a particularly convenient introductory text Thus it can be used to obtain a feel for the problems which designers are presently confronting and the solutions which they are developing Indeed the newcomer to the field with the benefit of a fresh and unbiased viewpoint may be in the best position to read through these collected reports and to identify some common point which has hitherto gone unnoticed or unexploited Second the book is aimed at the reader who is experienced in one particular area or discipline of non von Neumann computer design It serves as an introduction to hardware design for the computer scientist and as an introduction to parallel programming philosophy for the electronics engineer **EDN, Electrical Design News** ,1987 **Reduced Instruction Set Computers** William Stallings,1990

Proceedings of the Second International Conference on Massively Parallel Computing Systems ,1996 *Physical Level Interfaces and Protocols* Uyless D. Black,1988 **Journal of Information Science and Engineering** ,1996 Proceedings Institute of Electrical and Electronics Engineers,1993 Papers of the meeting held in Gran Canaria

Spain 27 29 January 1993 on architecture logic programming massively parallel processing algorithms communications methodologies tools environments simulation fault tolerance No index Acidic paper Annotation copyright Book News Inc Portland Desktop Publishing for the Writer Richard E. Ziegfeld, John Tarp, 1989 *Advances in Distributed System Reliability* Suresh Rai, Dharma Prakash Agrawal, 1990 Very Good No Highlights or Markup all pages are intact

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