



Network Processors

Shasha Hu



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Network Processors Ran Giladi,2008-08-29 Network processors are the basic building blocks of today's high speed high demand quality oriented communication networks Designing and implementing network processors requires a new programming paradigm and an in depth understanding of network processing requirements This book leads the reader through the requirements and the underlying theory of networks network processing and network processors It covers implementation of network processors and integrates EZchip Microcode Development Environment so that you can gain hands on experience in writing high speed networking applications By the end of the book the reader will be able to write and test applications on a simulated network processor Comprehensive theoretical and practical coverage of networks and high speed networking applications Describes contemporary core metro and access networks and their processing algorithms Covers network processor architectures and programming models enabling readers to assess the optimal network processor type and configuration for their application Free download from <http://www.cse.bgu.ac.il/npbook/> includes microcode development tools that provide hands on experience with programming a network processor

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Network Processor Design Mark A. Franklin,Patrick Crowley,Haldun Hadimioglu,Peter Z. Onufryk,2005-03-11 The past few years have seen significant change in the landscape of high end network processing In response to the formidable

challenges facing this emerging field the editors of this series set out to survey the latest research and practices in the design programming and use of network processors Through chapters on hardware software performance and modeling Network Processor Design illustrates the potential for new NP applications helping to lay a theoretical foundation for the architecture evaluation and programming of networking processors Like Volume 2 of the series Volume 3 further shifts the focus from achieving higher levels of packet processing performance to addressing other critical factors such as ease of programming application developments power and performance prediction In addition Volume 3 emphasizes forward looking leading edge research in the areas of architecture tools and techniques and applications such as high speed intrusion detection and prevention system design and the implementation of new interconnect standards Investigates current applications of network processor technology at Intel Infineon Technologies and NetModule Presents current research in network processor design in three distinct areas Architecture at Washington University St Louis Oregon Health and Science University University of Georgia and North Carolina State University Tools and Techniques at University of Texas Austin Academy of Sciences China University of Paderborn Germany and University of Massachusetts Amherst Applications at University of California Berkeley Universidad Complutense de Madrid Spain ETH Zurich Switzerland Georgia Institute of Technology Vrije Universiteit the Netherlands and Universiteit Leiden the Netherlands

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