

PowerPC™

PowerPC™ Microprocessor Common Hardware Reference Platform:

A SYSTEM ARCHITECTURE



IBM.



Powerpc Microprocessor Common Hardware Reference Platform A System Architecture

Clemens Wendtner



Powerpc Microprocessor Common Hardware Reference Platform A System Architecture:

PowerPC Microprocessor Common Hardware Reference Platform Apple Computer, Inc., International Business Machines, Inc., Motorola Corp., 2012-06-25 This book defines the architecture requirements and minimum system requirements for a computer system that is designed to become an open industry standard. These requirements provide a description of the devices interfaces and data formats required to design and build a PowerPC based computer. This standard is designed to provide software compatibility for several operating environments. Systems built to these requirements can use industry standard components currently found in IBM compatible and Apple Macintosh personal computers. These systems are expected to run various future versions of operating systems including Apple Mac OS™ IBM AIX™ and PowerPC™ Editions of IBM OS/2 Warp Connect™ Microsoft Windows NT™ Workstation Novell Netware™ and SunSoft Solaris™. This book is the primary source of information for anyone developing a hardware platform an operating system or hardware component to be part of these standard systems. It describes the hardware to operating system interface that is essential to anyone building hardware platforms and provides the minimum system configurations that platform designers must meet when building a standard platform. Component manufacturers require this information to produce compatible chips and adapters to use on these platforms and software developers require the information on mandatory functions and documented interfaces. The architecture is intended to support a range of PowerPC microprocessor based system implementations including portable desktop and server class systems and allows multiple operating system implementations across a wide range of environments and functions. This enables new hardware and software enhancements that are necessary for the development of improved user interfaces higher performance and broader operating environments. **PowerPC**

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description of the devices interfaces and dataformats required to design and build a PowerPC based computer This standard isdesigned to provide software compatibility for several operating environments Systems built to these requirements can use industry standard componentscurrently found in IBM compatible and Apple Macintosh personal computers Thesesystems are expected to run various future versions of operating systemsincluding Apple Mac OST IBM AIXT and PowerPCT Editions of IBM OS 2 Warp ConnectT Microsoft Windows NTT Workstation Novell NetwareT and SunSoft SolarisT This book is the primary source of information for anyone developing a hardwareplatform an operating system or hardware component to be part of thesestandard systems It describes the hardware to operating system interface thatis essential to anyone building hardware platforms and provides the minimumsystem configurations that platform designers must meet when building a standardplatform Component manufacturers require this information to producecompatible chips and adapters to use on these platforms and software developersrequire the information on mandatory functions and documented interfaces The architecture is intended to support a range of PowerPC microprocessor based system implementations including portable desktop and server classsystems and allows multiple operating system implementations across a widerange of environments and functions This enables new hardware and softwareenhancements that are necessary for the development of improved userinterfaces higher performance and broader operating environments

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different open source and free software packages commonly used in embedded Linux systems are also covered uClibc BusyBox U Boot OpenSSH tftpd tftp strace and gdb are among the packages discussed

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