The Technology of Parallel Processing: Parallel Processing Architectures and Very Large Scale Integration Hardware, Volume I

Decegama, Angel L.

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Parallel Processing On Very Large Scale Integration Arrays

O García

Parallel Processing On Very Large Scale Integration Arrays:

Third Caltech Conference on Very Large Scale Integration R. Bryant,2012-12-06 The papers in this book were presented at the Third Caltech Conference on Very Large Scale Integration held March 21 23 1983 in Pasadena California The conference was organized by the Computer Science Depart ment California Institute of Technology and was partly supported by the Caltech Silicon Structures Project This conference focused on the role of systematic methodologies theoretical models and algorithms in all phases of the design verification and testing of very large scale integrated circuits The need for such disciplines has arisen as a result of the rapid progress of integrated circuit technology over the past 10 years This progress has been driven largely by the fabrica tion technology providing the capability to manufacture very complex elec tronic systems reliably and at low cost At this point the capability to manufac ture very large scale integrated circuits has exceeded our capability to develop new product designs quickly reliably and at a reasonable cost As a result new designs are undertaken only if the production volume will be large enough to amortize high design costs products first appear on the market well past their announced delivery date and reference manuals must be amended to document design flaws Recent research in universities and in private industry has created an emerg ing science of very large scale integration VLSI for Pattern Recognition and Image Processing K.S. Fu,2013-03-08 During the past two decades there has been a considerable growth in interest in problems of pattern recognition and image processing PRIP This inter est has created an increasing need for methods and techniques for the design of PRIP systems PRIP involves analysis classification and interpretation of data Practical applications of PRIP include character recognition re mote sensing analysis of medical signals and images fingerprint and face identification target recognition and speech understanding One difficulty in making PRIP systems practically feasible and hence more popularly used is the requirement of computer time and storage This situation is particularly serious when the patterns to be analyzed are quite complex Thus it is of the utmost importance to investigate special comput er architectures and their implementations for PRIP Since the advent of VLSI technology it is possible to put thousands of components on one chip This reduces the cost of processors and increases the processing speed VLSI algorithms and their implementations have been recently developed for PRIP This book is intended to document the recent major progress in VLSI system design for PRIP applications Parallel Processing on VLSI Arrays Josef A. Nossek,2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publica tion on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical

systems i e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PD Es It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems ofPDEs involving time and finite propagation speed The basic ideas are explained using Maxwell s equa tions as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements **Scientific and Technical Aerospace Reports** ,1989 **Third Caltech Conference on Very Large Scale Integration** Randal E. Bryant,1983 Proceedings of the Caltech Conference on Very Large Scale Integration Charles L. Seitz,1979 VLSI Array Processors Sun Yuan Kung,1988 **VLSI Signal Processing** Peter R. Cappello,1984 Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology ,1999 **Real Time Signal Processing V** Joel Trimble,1982

Vector and Parallel Processors in Computational Science Iain S. Duff,John Ker Reid,1985 **ICASSP 88: V & E, VLSI, spectral estimation** ,1988 The Cumulative Book Index ,1994 A world list of books in the English language

Optics News ,1986 Includes a directory of members in one issue each year *EURO-PAR '... ,1999* Clock Distribution Networks in VLSI Circuits and Systems Eby G. Friedman,1995 Improve the performance and reliability of synchronous digital integrated circuits with this anthology of key literature on the design and analysis of clock distribution networks for VLSI based computer and signal processing systems Beginning with an extensive tutorial overview and bibliography this all in one source offers substantive coverage of the most relevant issues related to the design of clock distribution networks for application to high performance synchronous design Related topics include clock skew automated layout of clock nets distributed buffet and interconnect delays clock distribution design of structured custom VLSI circuits wafer scale integration systolic arrays globally asynchronous locally synchronous systems microwave issues low power clocking techniques process insensitive circuits deterministic and probabilistic delay models system timing specifications clock distribution networks of well known circuits and future research in clock distribution networks The material presented in Clock Distribution Networks in VLSI Circuits and Systems will be valuable to anyone with an interest in synchronous integrated circuits computer design or signal processing implementation issues **Conference Proceedings** ,1988

VLSI Signal Processing, VII Jan Rabaey,Paul M. Chau,John Eldon,1994 **High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures** K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and

extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures Design and Evaluation of VLSI Architectures for Matrix Problems Kam-Hoi Chang,1985

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